

FIG.1

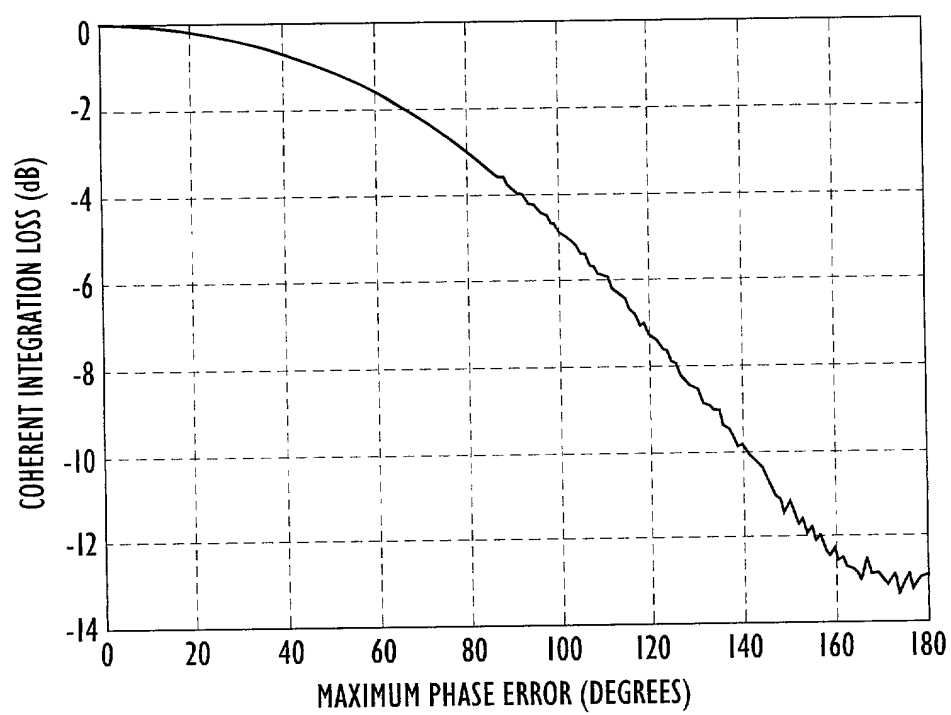


FIG.2

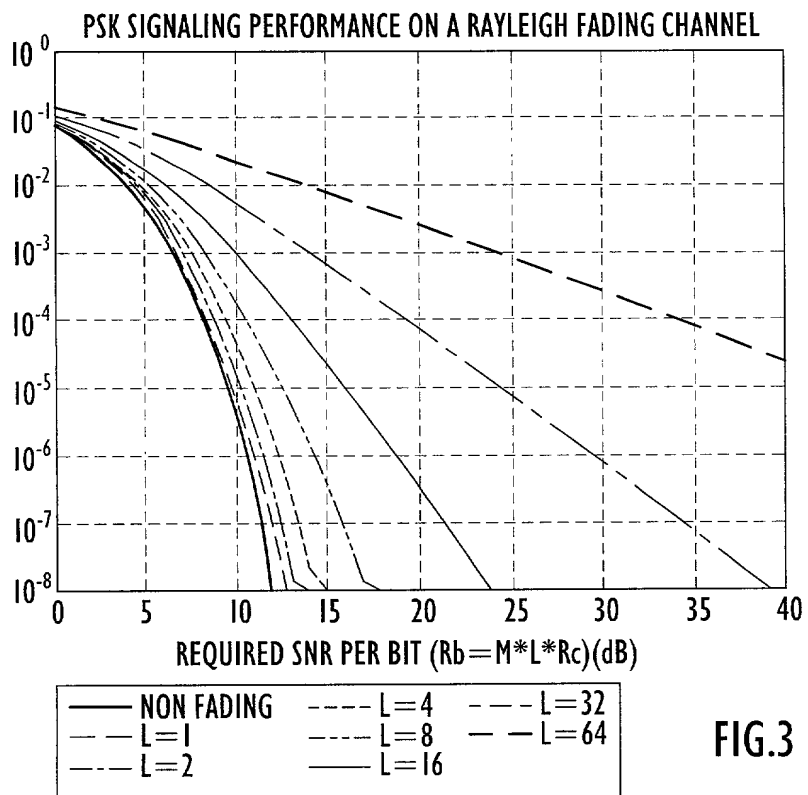


FIG.3

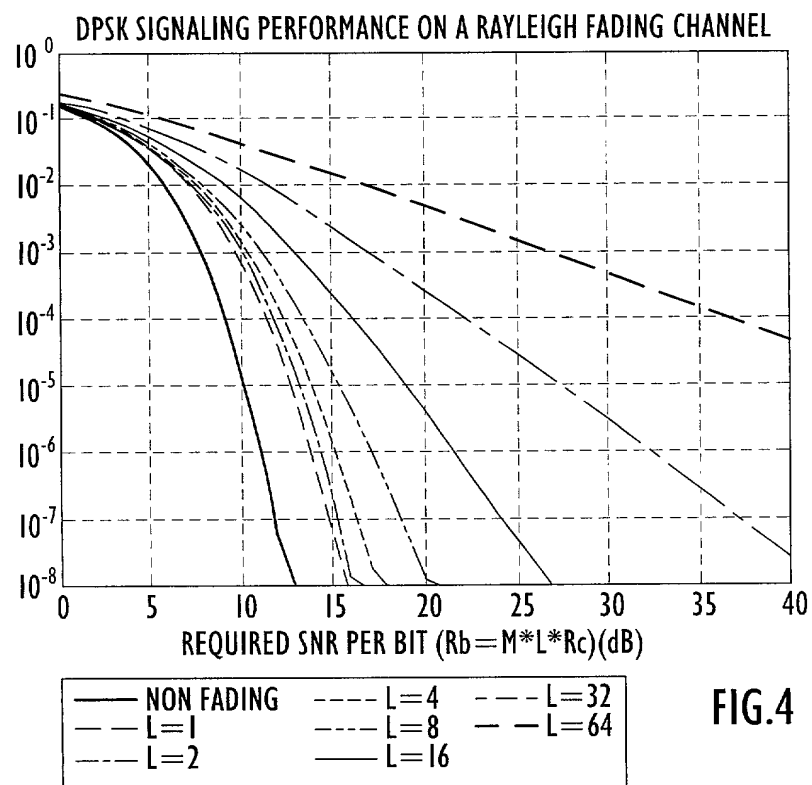


FIG.4

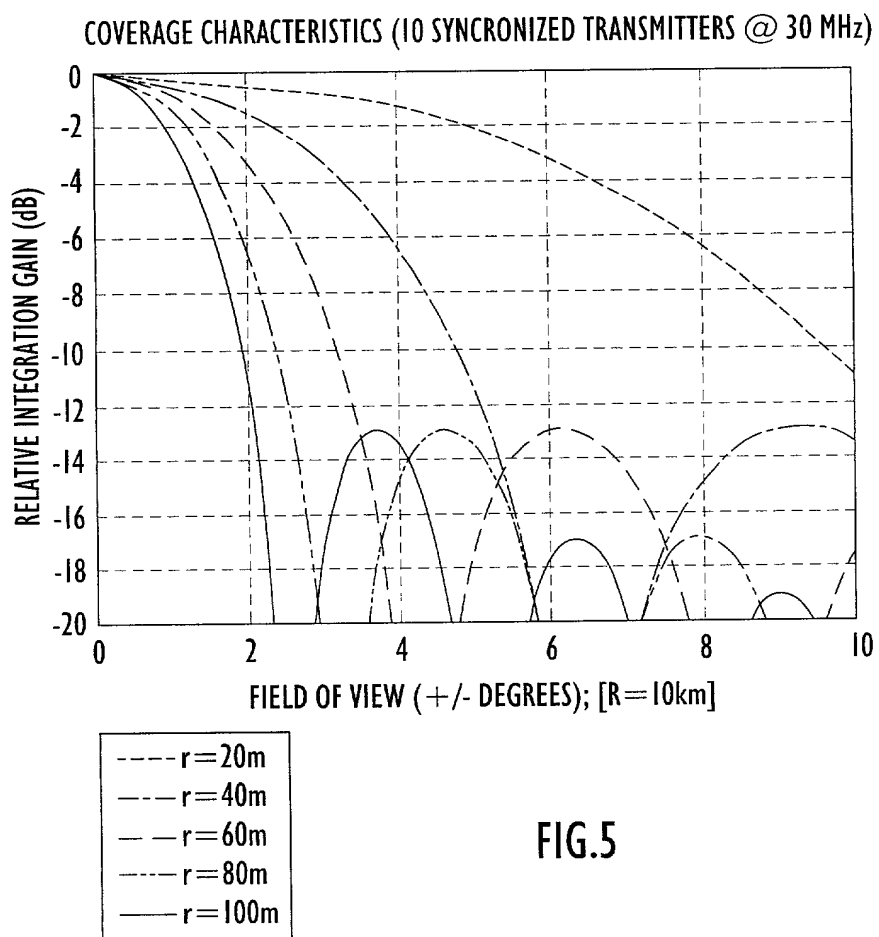


FIG.5

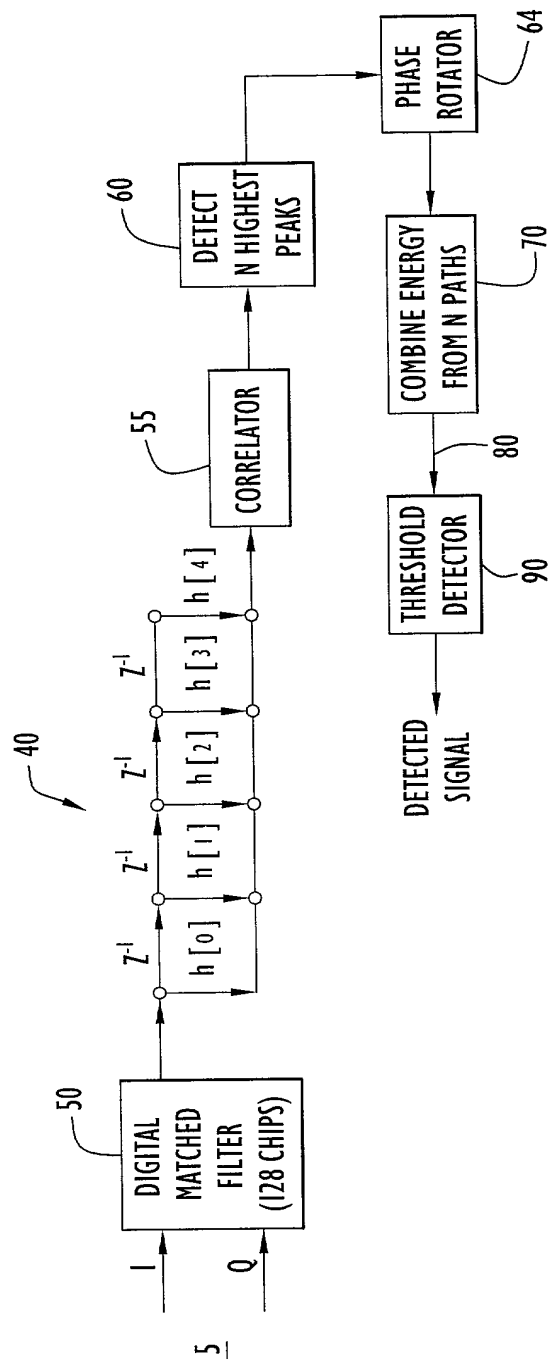
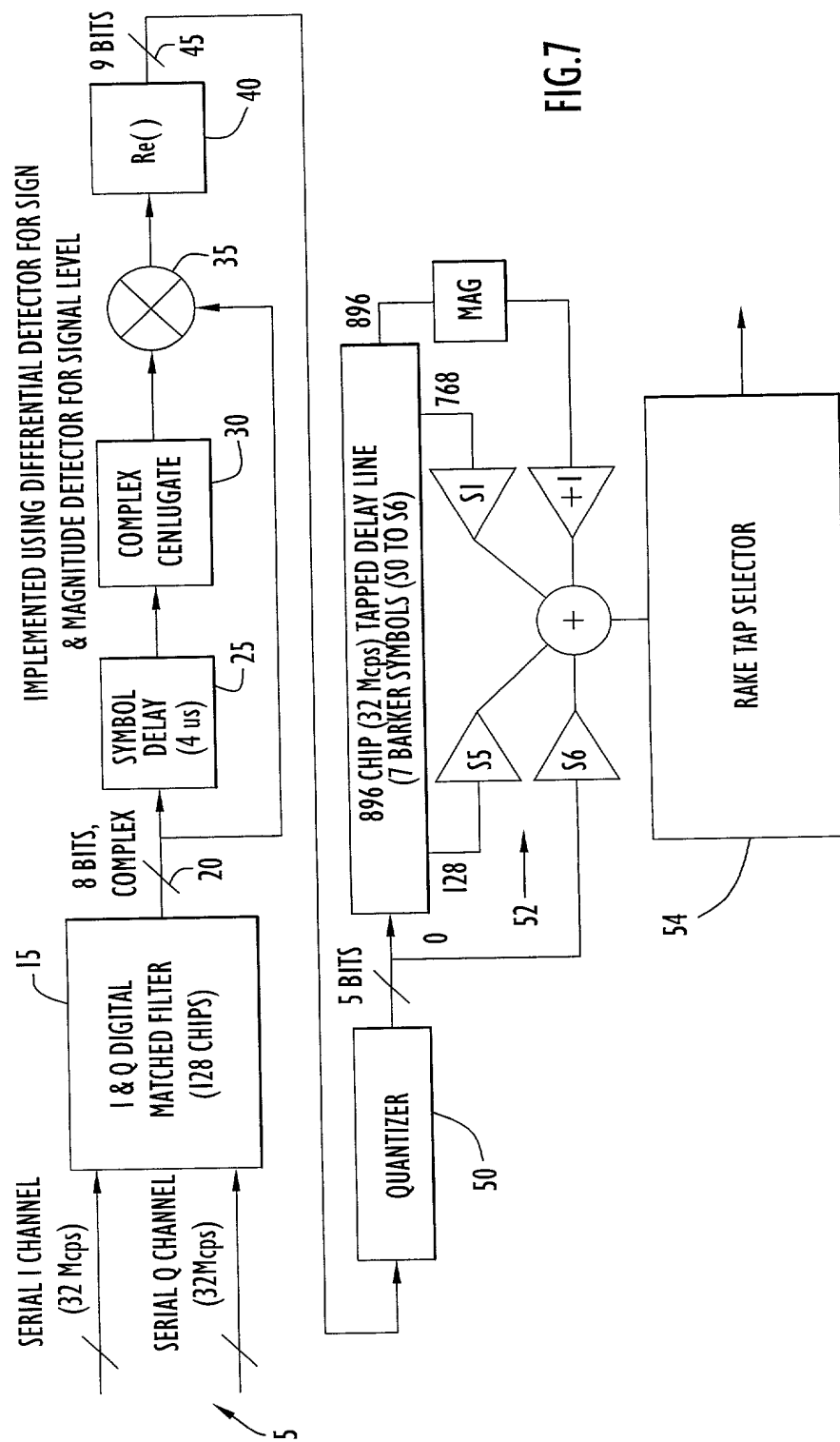


FIG.6



RTS/CTS/ACK ACQUISITION SEQUENCE 10 x 4 μ s SYMBOLS	REFERENCE																																	
	DESTINATION ADDRESS (7 BITS)							MESSAGE DATA RATE (2 BITS)		MESSAGE LENGTH (3 BITS)		MSG. CH. SUGGESTION (3 BITS)		NOISE CONTROL (2 BITS)		PKT. PRIORITY (5 BITS)					SOURCE ID (7 BITS)							RTS						
	SOURCE ADDRESS (7 BITS)							MESSAGE DATA RATE (2 BITS)		MESSAGE LENGTH (3 BITS)		MSG. CH. DIRECTION (2 BITS)		POWER CNTL. (3 BITS)		FEC (2 BITS)		SPARES (3 BITS)			DESTINATION ID (7 BITS)							CTS						
	SOURCE ADDRESS (7 BITS)							LINK QUALITY (3 BITS)		SPARES/NETWORK PROCESSOR WRITEABLE (12 BITS)																		DESTINATION ID (7 BITS)						

FIG.8a

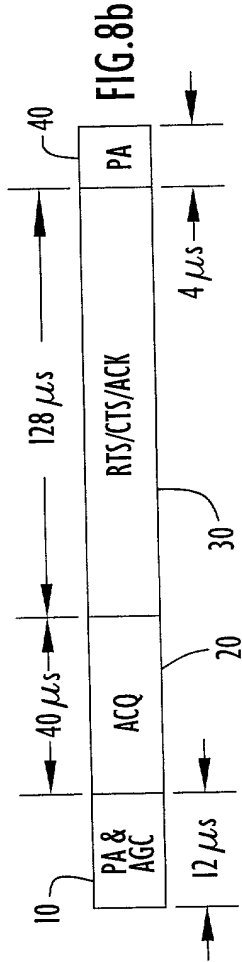


FIG.8b

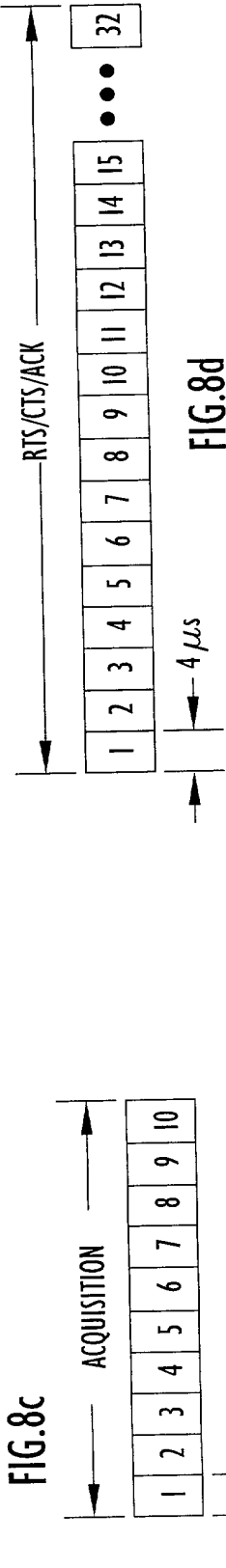


FIG.8c

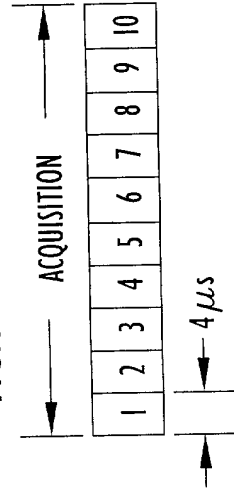
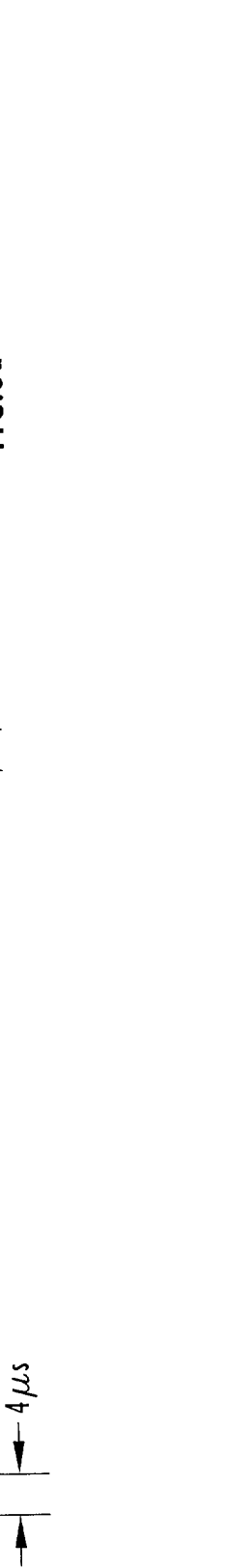


FIG.8d



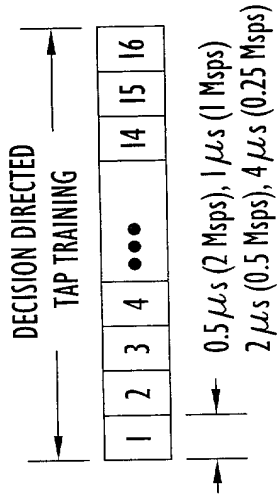
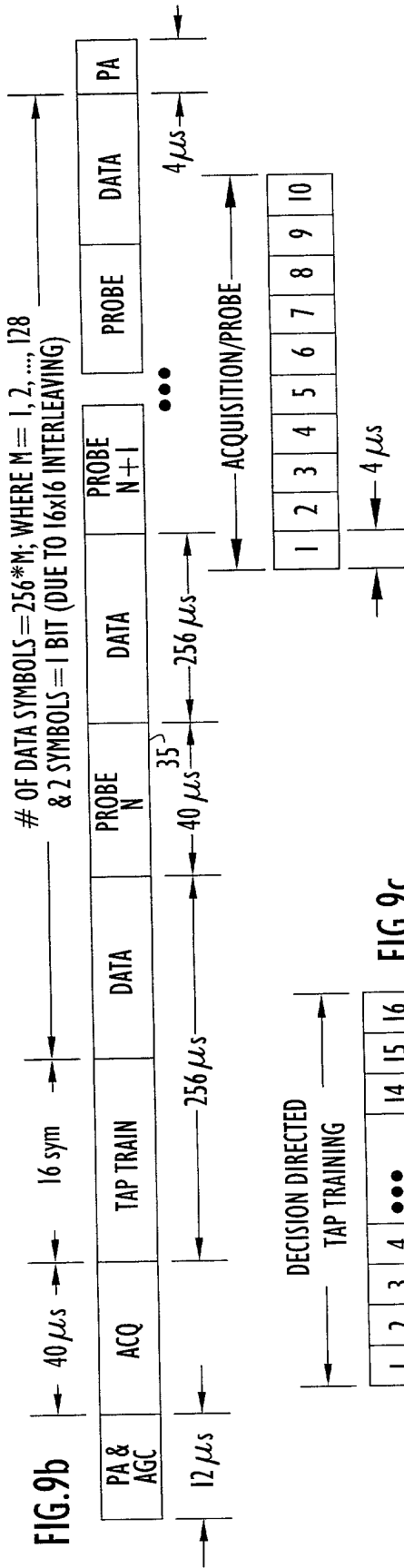
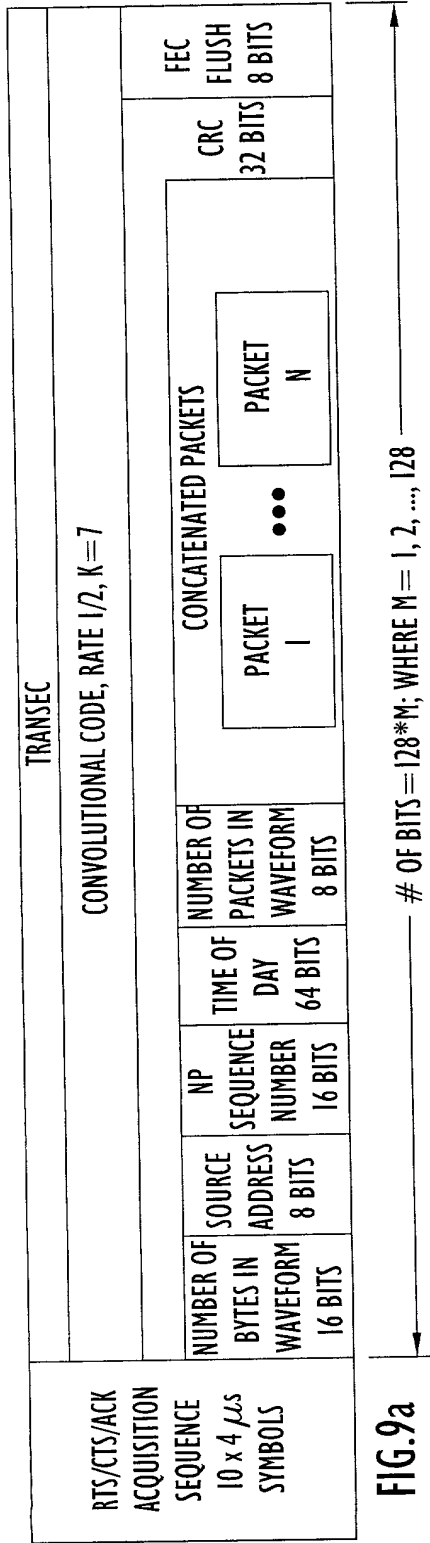


FIG.9d